

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A RAM-incorporated driver that drives a display section based on still-image data and moving-image data, the RAM-incorporated driver comprising:

a first port through which the still-image data or a given command is input from ~~an external MPU~~ a microprocessor unit (MPU) that is external to the RAM-incorporated driver;

a second port through which the moving-image data, which is transferred serially over a serial transfer line from the ~~external MPU~~ that is external to the RAM-incorporated driver, is input as a differential signal;

a reception circuit which differentially amplifies the differential signal input from the second port and creates the moving-image data in a parallel state;

a RAM which stores the still-image data that was input through the first port and the moving-image data that was created by the reception circuit;

a first control circuit which controls writing or reading of the still-image data or the moving-image data that has been input separately through the first port or the second port, with respect to the RAM; and

a second control circuit that, independently of the first control circuit, controls the reading of display data of the still-image data or moving-image data that has been stored in the RAM, and drives the display section to display.

2. (Previously Presented) The RAM-incorporated driver as defined by claim 1, further comprising:

a halt control circuit which receives with the differential signal a data validation signal indicating whether or not the differential signal is valid, and halts at least part of an operation of the reception circuit, based on the data validation signal.

3. (Previously Presented) The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal that synchronizes the writing of the moving-image data into the RAM.

4. (Previously Presented) The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal that synchronizes the writing of the moving-image data for one line of the display section into the RAM.

5. (Previously Presented) The RAM-incorporated driver as defined by claim 2, wherein the validation signal is used as a synchronization signal that synchronizes the writing of the moving-image data for one full-screen of the display section into the RAM.

6. (Original) The RAM-incorporated driver as defined by claim 1, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

7. (Original) The RAM-incorporated driver as defined by claim 2, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

8. (Original) The RAM-incorporated driver as defined by claim 3, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

9. (Original) The RAM-incorporated driver as defined by claim 4, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

10. (Original) The RAM-incorporated driver as defined by claim 5, wherein the serial transfer line is a transfer line in accordance with an LVDS standard.

11. (Original) The RAM-incorporated driver as defined by claim 1, wherein the serial transfer line is a transfer line in accordance with a USB standard.

12. (Original) The RAM-incorporated driver as defined by claim 2, wherein the serial transfer line is a transfer line in accordance with a USB standard.

13. (Original) The RAM-incorporated driver as defined by claim 3, wherein the serial transfer line is a transfer line in accordance with a USB standard.

14. (Original) The RAM-incorporated driver as defined by claim 4, wherein the serial transfer line is a transfer line in accordance with a USB standard.

15. (Original) The RAM-incorporated driver as defined by claim 5, wherein the serial transfer line is a transfer line in accordance with a USB standard.

16. (Original) The RAM-incorporated driver as defined by claim 1, wherein the serial transfer line is a transfer line in accordance with an IEEE 1394 standard.

17. (Original) The RAM-incorporated driver as defined by claim 2, wherein the serial transfer line is a transfer line in accordance with an IEEE 1394 standard.

18. (Original) The RAM-incorporated driver as defined by claim 3, wherein the serial transfer line is a transfer line in accordance with an IEEE 1394 standard.

19. (Original) The RAM-incorporated driver as defined by claim 4, wherein the serial transfer line is a transfer line in accordance with an IEEE 1394 standard.

20. (Original) The RAM-incorporated driver as defined by claim 5, wherein the serial transfer line is a transfer line in accordance with an IEEE 1394 standard.

21. (Previously Presented) A display unit, comprising:  
a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;  
the RAM-incorporated driver as defined by claim 1, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

22. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 2, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

23. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second;

the RAM-incorporated driver as defined by claim 3, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

24. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 4, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

25. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 5, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

26. (Previously Presented) Electronic equipment, comprising:  
the display unit as defined by claim 21; and  
the MPU which supplies the command, the still-image data, and the moving-image data to the display unit.

27. (Previously Presented) A RAM-incorporated driver that drives a display section based on still-image data and moving-image data, the RAM-incorporated driver comprising:

a first port through which the still-image data or a given command is input;  
a second port, independent from the first port, through which the moving-image data which is transferred serially over a serial transfer line, is input as a differential signal;

a reception circuit which differentially amplifies the differential signal input from the second port and creates the moving-image data in a parallel state;

a RAM which stores the still-image data that was input through the first port and the moving-image data that was created by the reception circuit;

a first control circuit which controls writing or reading of the still-image data or the moving-image data that has been input separately through the first port or the second port, with respect to the RAM; and

a second control circuit that, independently of the first control circuit, controls the reading of display data of the still-image data or moving-image data that has been stored in the RAM, and drives the display section to display,

wherein the still-image data can be rewritten irrespective of the timing at which the moving-image data is rewritten in the RAM.

28. (Previously Presented) A RAM-incorporated driver that drives a display section based on still-image data and moving-image data, the RAM-incorporated driver comprising:

a first port through which the still-image data or a given command is input;

a second port, independent from the first port, through which the moving-image data which is transferred serially over a serial transfer line, is input as a differential signal;

a reception circuit which differentially amplifies the differential signal input from the second port and creates the moving-image data in a parallel state;

a RAM which stores the still-image data that was input through the first port and the moving-image data that was created by the reception circuit;

a first control circuit which controls writing or reading of the still-image data and the moving-image data that has been input separately through the first port or the second port, with respect to the RAM; and

a second control circuit that, independently of the first control circuit, controls the reading of display data of the still-image data or moving-image data that has been stored in the RAM, and drives the display section to display.

29. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 27, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

30. (Previously Presented) A display unit, comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver as defined by claim 28, which drives the plurality of first electrodes; and

a scanning driver that scans and drives the plurality of second electrodes.

31. (Currently Amended) Electronic equipment, comprising:

the display unit as defined by claim 29; and

~~the MPU~~ a microprocessor unit which supplies the command, the still-image data, and the moving-image data to the display unit.

32. (Currently Amended) Electronic equipment, comprising:

the display unit as defined by claim 30; and

~~the MPU~~ a microprocessor unit which supplies the command, the still-image data, and the moving-image data to the display unit.

33. (Previously Presented) The RAM-incorporated driver as defined by claim 27, wherein the first port is not directly connected to the second port.

34. (Previously Presented) The RAM-incorporated driver as defined by claim 28, wherein the first port is not directly connected to the second port.